



AYDIN ADNAN MENDERES UNIVERSITY COURSE INFORMATION FORM

Course Title		Digital Electronic							
Course Code		ELE211		Course Level		Short Cycle (Associate's Degree)			
ECTS Credit	2	Workload	50 (Hours)	Theory	2	Practice	0	Laboratory	0
Objectives of the Course		In this course, it is aimed to have the students gain the abilities and knowledge about making of basic logic circuits, logic circuits simplification methods, logic circuits, attaining electrical equivalents, building up and operating necessary circuit by making a solution of a given application problem.							
Course Content		Base arithmetics, number systems, logic doors and circuits, karnaugh maps							
Work Placement		N/A							
Planned Learning Activities and Teaching Methods				Explanation (Presentation), Demonstration, Problem Solving					
Name of Lecturer(s)		Ins. İsmail MERSİNKAYA							

Assessment Methods and Criteria

Method	Quantity	Percentage (%)
Midterm Examination	1	40
Final Examination	1	70

Recommended or Required Reading

1	Digital Electronics(Yılmaz Çamurcu)
2	Logic Circuits (Prof.Dr.Hüseyin Ekiz)

Week	Weekly Detailed Course Contents	
1	Theoretical	Number systems
2	Theoretical	Number systems
3	Theoretical	Logical gate circuits
4	Theoretical	Logical gate circuits
5	Theoretical	Integrated circuit families and technical properties
6	Theoretical	Circuit drawing from logic functions Finding the logic function of a drawn circuit
7	Theoretical	Circuit drawing from logic functions Finding the logic function of a drawn circuit.
8	Theoretical	Boolean Mathematics
9	Theoretical	Boolean Mathematics
10	Theoretical	Karnaugh map
11	Theoretical	Karnaugh map
12	Theoretical	Deriving the logic function of a problem and simplification
13	Theoretical	Forming the time diagram of a problem
14	Theoretical	Building up and operating the logic circuit of a problem

Workload Calculation

Activity	Quantity	Preparation	Duration	Total Workload
Lecture - Theory	14	1	1	28
Assignment	5	0	1	5
Midterm Examination	1	7	1	8
Final Examination	1	8	1	9
Total Workload (Hours)				50
[Total Workload (Hours) / 25*] = ECTS				2

*25 hour workload is accepted as 1 ECTS

Learning Outcomes

1	Making of basic logic circuits,
2	Simplification of logic circuits



3	Solving, building up circuits and operating of the logic problems
4	Karnaugh can edit the map.
5	Can create control circuits with digital logic circuits.

Programme Outcomes (Electrics)

1	ABILITY TO MAKE APPLICATIONS OF MEASUREMENT AND CALCULATION
2	ABILITY TO MAKE CONNECTIONS OF A DC CIRCUIT
3	ABILITY TO MAKE BASIC ELECTRONIC CIRCUIT AND APPLICATIONS
4	ABILITY TO MAKE ELECTRIC INSTALLMENT APPLICATIONS
5	ADAPTING VOCATIONAL ETHICAL VALUES
6	ABILITY TO MAKE COMMUNICATION
7	ABILITY TO MAKE CONNECTIONS OF AC CIRCUIT
8	ABILITY TO MAKE NUMERICAL CIRCUITS
9	ABILITY TO MAKE INSTALLATIONS OF TRANSFORMER AND DC ELECTRIC MACHINES
10	ABILITY TO MAKE COMPUTER AIDED DESIGN
11	ABILITY TO APPLY VOCATIONAL TECHNICAL METHODS
12	ABILITY TO MAKE INSTALLATIONS OF AC ELECTRIC MACHINES
13	ABILITY TO MAKE SPECIAL ELECTRIC INSTALLMENTS
14	ABILITY TO MAKE INSTALLMENTS OF COMMAND SYSTEMS
15	ABILITY TO DRAW COMPUTER AIDED ELECTRIC SCHEME
16	ABILITY TO MAKE POWER ELECTRONICS CIRCUITS
17	ABILITY TO MAKE SYSTEM ANALYSIS AND PRODUCT DESIGN
18	ABILITY TO IMPROVE ONESELF UTILIZING INFORMATION OPPORTUNITIES
19	ABILITY TO DRAW COMPUTER AIDED ELECTRIC INSTALLMENT PROJECT
20	ABILITY TO MAKE ANALYSIS AND MAINTENANCE OF ELECTRICAL ENERGY PRODUCTION SYSTEMS
21	ABILITY TO MAKE THE WINDING OF ACCURATE AND ALTERNATIVE CURRENT ENGINES
22	ABILITY TO RECOGNIZE SYSTEMS USED IN ELECTRICAL ENERGY TRANSMISSION AND DISTRIBUTION AND TROUBLESHOOTING
23	Ability to use the methods and techniques of career planning and discussing the effects of character traits on career preferences.
24	Ability to plan a career in their own profession.
25	To provide them with knowledge about substance use and addiction problem and prevention methods.

Contribution of Learning Outcomes to Programme Outcomes 1:Very Low, 2:Low, 3:Medium, 4:High, 5:Very High

	L1	L2	L3	L4	L5
P1	4	4	3		4
P3			3		
P8	5	5	5		5
P14	3	4	4		5
P17	4	5	4	5	5

