

## AYDIN ADNAN MENDERES UNIVERSITY COURSE INFORMATION FORM

Course Title		Seminar							
Course Code		EEE701		Couse Level		Second Cycle (Master's Degree)			
ECTS Credit	6	Workload	144 (Hours)	Theory	0	Practice	2	Laboratory	0
Objectives of t	the Course	The course aims to gain research, synthesize and analysis processes of a specific subject determined by the student in order to work in the consultancy of a professor and present the final report during the master program.							
Course Content		Literature research, collecting data, compilation, analysis, present the results as a seminar.							
Work Placement		N/A							
Planned Learning Activities and Teaching Methods			Explanation (Presentation), Discussion, Project Based Study, Individual Study						
Name of Lecturer(s)  Assoc. Prof. Coşkun DENİZ, Assoc. Prof. Münevver Mine ÖZYETKİN, Lec. İsmail YARİÇİ, Prof. Old ÜZENGİ AKTÜRK				Olcay					

Assessment Methods and Criteria							
Method	Quantity	Percentage (%)					
Seminar	1	100					

## **Recommended or Required Reading**

1 Literature researchs about the subject of the seminar.

Week	Weekly Detailed Co	urse Contents
1	Theoretical	Weekly discussion with supervisor
2	Theoretical	Weekly discussion with supervisor
3	Theoretical	Weekly discussion with supervisor
4	Theoretical	Weekly discussion with supervisor
5	Theoretical	Weekly discussion with supervisor
6	Theoretical	Weekly discussion with supervisor
7	Theoretical	Weekly discussion with supervisor
8	Theoretical	Weekly discussion with supervisor
9	Theoretical	Weekly discussion with supervisor
10	Theoretical	Weekly discussion with supervisor
11	Theoretical	Weekly discussion with supervisor
12	Theoretical	Weekly discussion with supervisor
13	Theoretical	Weekly discussion with supervisor
14	Theoretical	Weekly discussion with supervisor
15	Theoretical	Weekly discussion with supervisor
16	Theoretical	Seminar

Workload Calculation							
Activity	Quantity	Preparation	Duration	Total Workload			
Seminar	1	100	2	102			
Individual Work	14	0	3	42			
Total Workload (Hours)							
[Total Workload (Hours) / 25*] = <b>ECTS</b>							
*25 hour workload is accepted as 1 ECTS							

Learn	Learning Outcomes							
1	To be able to research the literature related to choose subject.							
2	To be able to synthesize, analyse and interpret the information obtained.							
3	To be able to write a report on the results.							
4	To be able to present the outcomes.							



To be able to discuss the results of the report with other persons.

## Programme Outcomes (Electrical and Electronics Engineering Master)

- Developing and intensifying knowledge that requires expertise in the area of Electrical-Electronics Engineering, and gaining the skills necessary to analyze and solve problems using this knowledge
- Grasping the inter-disciplinary interaction related to Electrical-Electronics Engineering, interpreting and forming new types of knowledge by combining the knowledge from Electrical-Electronics Engineering and the knowledge from various other disciplines
- Developing new approaches to solve the complex problems arising in Electrical-Electronics Engineering, coming up with solutions while taking responsibility and carrying out a specific study independently
- 4 Assessing the knowledge and skill gained in the area of Electrical-Electronics Engineering with a critical view
- 5 Transferring the current developments and one's own work in Electrical-Electronics Engineering, to other groups in written, oral and visual forms
- The ability to control the collecting, interpreting, practicing and announcing processes of the Electrical-Electronics Engineering related to data taking into consideration scientific, cultural and ethical values and the ability to teach these values to others
- 7 Developing application plans concerning the subjects related to Electrical-Electronics Engineering and the ability to evaluate the end results of these plans within the frame of quality processes

## Contribution of Learning Outcomes to Programme Outcomes 1: Very Low, 2:Low, 3: Medium, 4: High, 5: Very High

	L1	L2	L3	L4	L5
P1	5	5	4		4
P2	5	5	4	4	4
P3	5	5	4	4	4
P4	5	5	4	4	4
P5	5	5	4	4	4
P6	5	5	4	4	4
P7	5	5	5	4	4

